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Li Mei Vermilya

Date: June 17, 2005

PATENT APPLICATION
Docket No. 8750-040
Client Ref. No. SPX200211-0043US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Hun KIM and Kyu-Whan CHONG
Serial No.: 10/666,507 Examiner: Perkins, Pamela E.
Filed: September 19, 2003 Art Unit: 2822
Confirmation No.: 9186
For: MOS TRANSISTOR HAVING A RECESSED GATE ELECTRODE AND
FABRICATION METHOD THEREOF

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

- ☒ Publication (\$300) and Issue Fee (\$1400.00 large)
- ☒ In connection with issuance of a patent:
 - ☐ Supplemental Declaration
 - ☒ PTO Form 85B
- ☒ Applicant's Comments on Examiner's Statement of Reasons for Allowance
- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,
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**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

In the Statement of Reasons for Allowance section, the Examiner recited portions of the allowed independent claims and stated that the prior art did not anticipate, teach or suggest the recited portions. The applications note that it is a well known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

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Respectfully submitted,

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